

ABSTRACT OF THE DISCLOSURE

A solid-state image pickup device has a discharge gate and a discharge drain provided adjacent to a connection of a vertical CCD and a horizontal CCD so that charges accumulated for an arbitrary pixel can be completely depleted. Data can be read at an arbitrary decimation rate only by changing a drive condition of the discharge gate. An arbitrary decimation rate can be achieved and a frame rate, resolution or the like can easily changed while vertical transfer electrodes can keep the same wiring structure as in a still mode (normal reading) without making a complicated wiring structure of vertical transfer electrodes such as the prior art. Therefore, this solid-state image pickup device can achieve an arbitrary decimation rate and easily change a frame rate, resolution or the like only by changing drive conditions without making a complicated wiring structure of vertical transfer electrodes.

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